TOSHIBA Field Effect Transistor Silicon N Channel Dual Gate MOS Type

3SK249

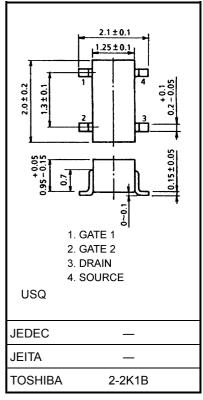
TV Tuner, UHF RF Amplifier Applications

Unit: mm

- Superior cross modulation performance.
- Low reverse transfer capacitance: $C_{rss} = 20 \text{ fF (typ.)}$
- Low noise figure.: NF = 1.5dB (typ.)

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DS}	12.5	V
Gate 1-source voltage	V _{G1S}	<u>±</u> 8	٧
Gate 2-source voltage	V _{G2S}	±8	V
Drain current	I _D	30	mA
Drain power dissipation	P _D	100	mW
Channel temperature	T _{ch}	125	°C
Storage temperature range	T _{stg}	−55~125	°C

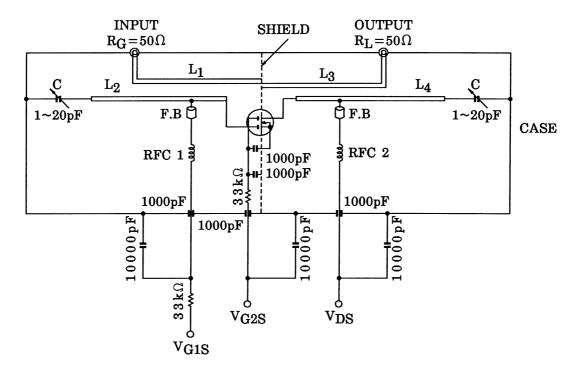


Weight: 0.006 g (typ.)

Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate 1 leakage current	I _{G1SS}	$V_{DS} = 0$, $V_{G1S} = \pm 6$ V, $V_{G2S} = 0$	_	_	±50	nA
Gate 2 leakage current	I _{G2SS}	$V_{DS} = 0$, $V_{G1S} = 0$, $V_{G2S} = \pm 6 \text{ V}$	_	_	±50	nA
Drain-source voltage	V _(BR) DSX	$V_{G1S} = -0.5 \text{ V}, V_{G2S} = -0.5 \text{ V}$ $I_D = 100 \mu\text{A}$	12.5	_	_	V
Drain current	I _{DSS}	$V_{DS} = 6 \text{ V}, V_{G2S} = 4.5 \text{ V}, V_{G1S} = 0 \text{ V}$	0	_	0.1	mA
Gate 1-source cut-off voltage	V _{G1S} (OFF)	$V_{DS} = 6 \text{ V}, V_{G2S} = 4.5 \text{ V}, I_D = 100 \mu A$	0.4	0.9	1.4	V
Gate 2-source cut-off voltage	V _{G2S} (OFF)	$V_{DS} = 6 \text{ V}, V_{G1S} = 4.0 \text{ V}, I_D = 100 \mu\text{A}$	0.5	1.0	1.5	V
Forward transfer admittance	Y _{fS}	$V_{DS} = 6 \text{ V}, V_{G2S} = 4.5 \text{ V}, I_D = 10 \text{ mA}, f = 1 \text{ kHz}$	17	21	_	mS
Input capacitance	C _{iss}	V _{DS} = 6 V, V _{G2S} = 4.5 V, I _D = 10 mA,	0.9	1.5	2.1	pF
Reverse transfer capacitance	C _{rss}	f = 1 MHz	_	20	40	fF
Power gain	G _{ps}	V _{DS} = 6 V, V _{G2S} = 4.5 V, I _D = 10 mA,	18	20	_	dB
Noise figure	NF	f = 800 MHz	_	1.5	2.5	dB

1



 $L_1\sim L_4$: $\phi 0.8$ mm silver plated copper wire

C: Air trimmer TTA25A200A (MURATA Manufacturing, Co., Ltd.)

RFC 1: ϕ 0.35 mm copper wire 3 mm ID, 7 T

RFC 2: $\phi 0.35$ mm copper wire 3 mm ID, 10 T

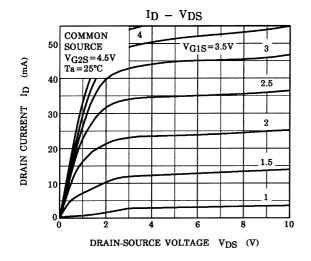
Figure 1 G_{ps}, NF Test Circuit

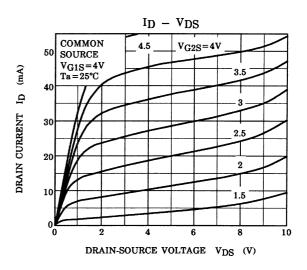
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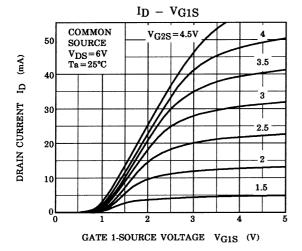
Marking

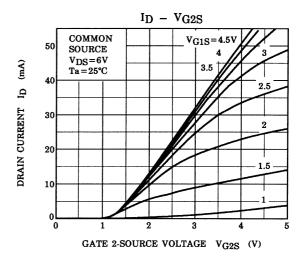


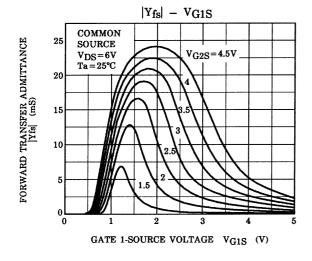
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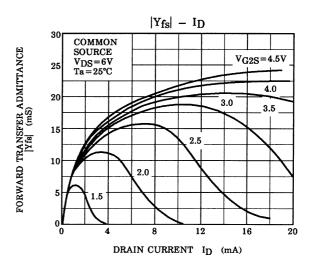








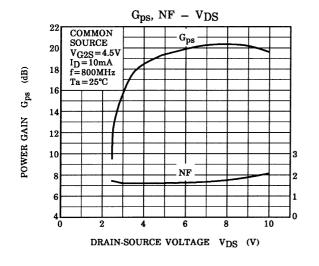


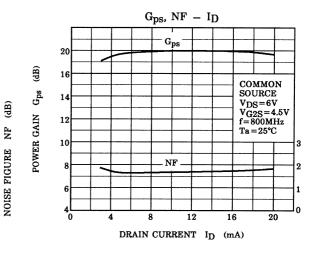


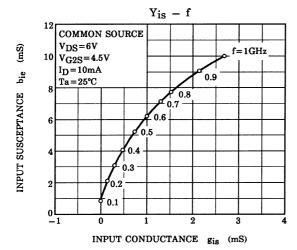
(dB)

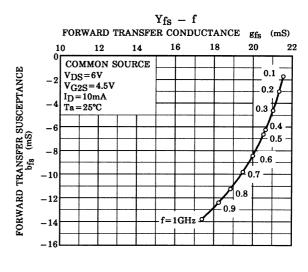
NF

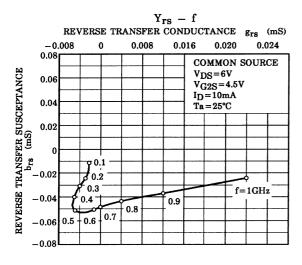
NOISE FIGURE

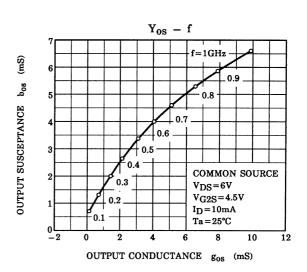


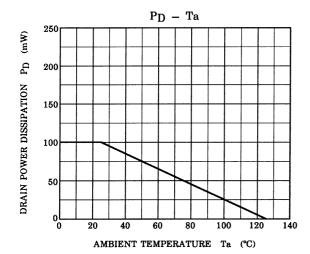












5 2003-04-04

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